Docket No. 240522US2S

IN RE APPLICATION OF: Yoshihisa IWATA

SERIAL NO: 10/623,559 FILED: July 22, 2003

FOR: SEMICONDUCTOR APPARATUS HAVING LOGIC LEVEL DECISION CIRCUIT AND INTER-

SEMICONDUCTOR APPARATUS SIGNAL TRANSMISSION SYSTEM

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Transmitted herewith is an amendment in the above-identified application.

	No	additional	fee	is	rec	mire	ed
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- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☐ Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS		RATE		CALCULATIONS
TOTAL	8	MINUS	21	0	x	\$50	=	\$0.00
INDEPENDENT	2	MINUS	5	0	x	\$210		\$0.00
APPLICATION SIZE		MINUS	100	0 (each addtl. 50 sheets)	x	\$260	===	\$0.00
	☐ MULTIPLE DEPENDENT CLAIMS + \$370 =				\$0.00			
	TOTAL OF ABOVE CALCULATIONS					\$0.00		
9	☐ Reduction by 50% for filing by Small Entity					\$0.00		
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- \square Online credit card payment is being made to cover the fees in the amount of \$0.00
- Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030.

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

Registration No. 28,870

Customer Number

22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03) Kurt M. Berger, Ph.D. Registration No. 51,461